This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

1. (Currently amended) Method for setting an operating parameter in a peripheral IC ~~of an electronic appliance, in which method the~~ comprising:

transmitting an operating parameter ~~is transmitted~~ from a central IC ~~in the electronic appliance~~ via a bus connection to the peripheral IC,

~~wherein~~ buffering the operating parameter ~~is buffered~~ in a preregister of the peripheral IC ~~while a working process is running with the~~ with a current operating parameter stored in a working register, ~~and~~

sending a transfer signal from the central IC via the bus connection to the peripheral IC,

~~and that the buffered operating parameter is transferred~~ transferring the buffered operating parameter to said working register ~~to become active in the working process only if a~~ if said transfer signal has been ~~sent from the central IC via the bus connection~~ received, wherein the bus connection is a serial bus connection with a data line, a control line, and a clock line,

signaling start of a data transmission from the central IC to the peripheral IC as well as the transfer signal via said control line, in which method the start signal is transmitted on the control line with a rising or falling edge of a system clock signal during a phase of a clock signal on the clock line and the transfer signal is transmitted on the control line in a phase where no clock signal is present on the clock line.

2. (Cancelled).

3. (Cancelled).

4. (Currently amended) Method according to claim ~~2~~ 1, ~~in which~~ further comprising transferring the register write address for writing to the preregister ~~is transferred to~~ in the peripheral IC on the data line ahead of the operating parameter.

5. (Cancelled).

6. (Currently amended) Device for carrying out the method as claimed in claim 1 with a central IC and a peripheral IC, with a bus connection between the central IC and the peripheral IC, where the peripheral IC has a working register for an operating parameter,

wherein the peripheral IC also has a preregister for buffering an operating parameter ~~while a working process is running with the current operating parameter stored in a working register~~, said operating parameter is received via the bus connection, and wherein the device has means for transferring the buffered value to the working register to become active in the working process, which means respond to a transfer signal that is ~~transmitted~~ received from the central IC via the bus connection, and

further wherein the bus connection is a serial bus connection with a data line, a control line, and a clock line, further comprising means for transmitting a start signal for data transmission from the central IC to the peripheral IC over the control line and means for transmitting the transfer signal from the central IC to the peripheral IC, including signaling means according to which the start signal is transmitted on the control line with a rising or falling edge of a system clock during a phase of a clock signal on the clock line and the transfer signal is transmitted on the control line in a phase where no clock signal is present on the clock line.

7. (Cancelled).

8. (Cancelled).

9. (Currently amended) Device according to ~~claims 7~~ claim 6, including bus protocol means according to which ~~the~~ a register write address for writing to the preregister is transferred to the peripheral IC on the data line ahead of the operating parameter.

10. (Cancelled).

11. (Previously presented) Device according to claim 6, in which device the peripheral IC relates to a front-end IC for a communication arrangement for wireless data transmission and the central IC relates to a signal processing device, with means for modulation or demodulation of the mixed RF input signal and for further signal processing in baseband.

12. (Currently amended) Device as claimed in claim 11, in which device the operating parameter relates to ~~the~~ a gain setting for ~~the~~ a receive gain in the front-end IC.

13. (Previously presented) Device according to claim 6, which device is configured as a send and receive device for wireless data transmission in accordance with the HIPERLAN2 standard.